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List of Patents and Publications To TRA

Y. DKT. NO. 5500-98000

SERIAL NO. 10/822,506

For Applicant's Information

APPLICANT: John V. Sell

**GROUP: 2173** 

Disclosure Statement (Use several sheets if necessary)

FILING DATE: April 12, 2004

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		U	.S. PATENT	DOCUMENTS			
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE APPROPRIAT
ZUS		5,949,423	9/7/99	Olsen			
		6,226,012	5/1/01	Priem, et al.			
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DNS		6,266,064	7/24/01 .	Snyder			
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EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO YES/NO
		<u> </u>					
		OTHER ART (	ncluding Autho	or, Title, Date, Pertinent I	Pages Etc.)		
±w	Al			ling - NVIDIA's New GeFor	<del></del>	bruary 27.	2001, 16
1	A2	Alekey Berillo, "S3TC and FXT1 Texture Compression," Copyright by Digit-Life.com, 1997-2003, 21 pages.					
	A3	"Geode™ GX1 Processor Series Low Power Integrated x86 Solution," National Semiconductor Corporation, June 2002, 246 pages.					
MA	A4	Ned Greene, Michael Kass, Gavin Miller, "Hierarchical Z-Buffer Visibility," Siggraph Conference Proceedings, August 1993.					

**EXAMINER:** 

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent own